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L1	23713	"710"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/27 14:53
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L3	22268	"713"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/27 14:43
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Phadke, A.G.; Pickett, B.; Adamiak, M.; Begovic, M.; Benmouyal, G.; Burnett, R.O., Jr.; Cease, T.W.; Goossens, J.; Hansen, D.J.; Kezunovic, M.; Mankoff, L.L.; McLaren, P.G.; Michel, G.; Murphy, R.J.; Nordstrom, J.; Sachdev, M.S.; Smith, H.S.; Thorp, J.S.; Trotignon, M.; Wang, T.C.; Xavier, M.A.;

Power Delivery, IEEE Transactions on
Volume 9, Issue 1, Jan. 1994 Page(s):442 - 452
Digital Object Identifier 10.1109/61.277716

[AbstractPlus](#) | Full Text: [PDF](#)(1172 KB) IEEE JNL**2. Refine and gabriel: support for refinement and tactics**

Oliveira, M.; Xavier, M.; Cavalcanti, A.;

Software Engineering and Formal Methods, 2004. SEFM 2004. Proceedings of the Second International Conference on
28-30 Sept. 2004 Page(s):310 - 319

Digital Object Identifier 10.1109/SEFM.2004.1347535

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 Kramer, A.; Fabbizio, V.; Mariaud, X.; Raynal, F.;
 Solid-State Circuits Conference, 1998. Digest of Technical Papers. 45th ISSCC 1998 IEEE
 International
 5-7 Feb. 1998 Page(s):196 - 197, 437
 Digital Object Identifier 10.1109/ISSCC.1998.672433
[AbstractPlus](#) | Full Text: [PDF](#)(620 KB) IEEE CNF
- ☐ 2. **Low power, low voltage conductance-mode CMOS analog neuron**
 Fabbizio, V.; Raynal, F.; Mariaud, X.; Kramer, A.; Colli, G.;
 Microelectronics for Neural Networks, 1996., Proceedings of Fifth International Conference on
 12-14 Feb. 1996 Page(s):111 - 115
 Digital Object Identifier 10.1109/MNNFS.1996.493780
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